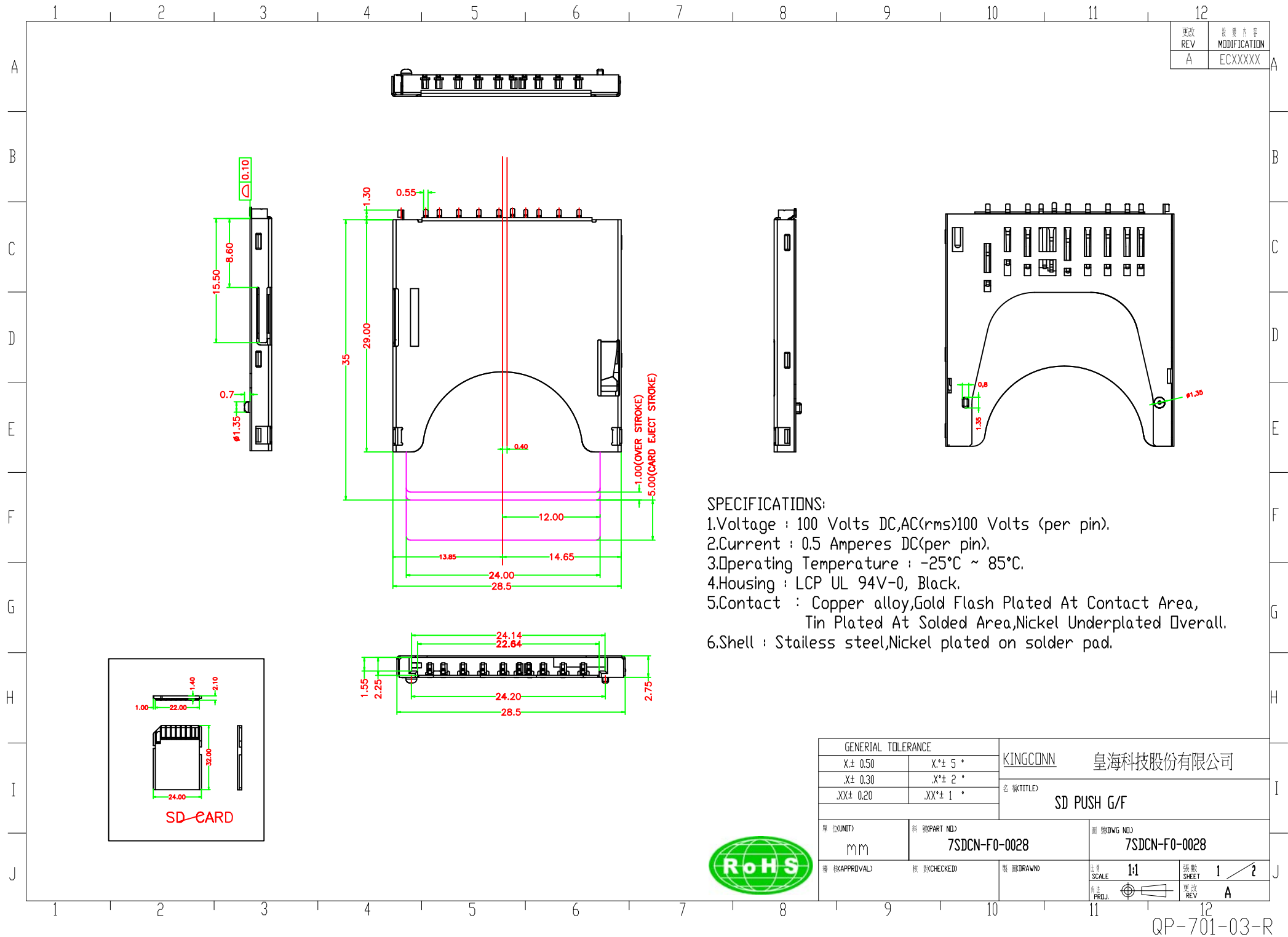
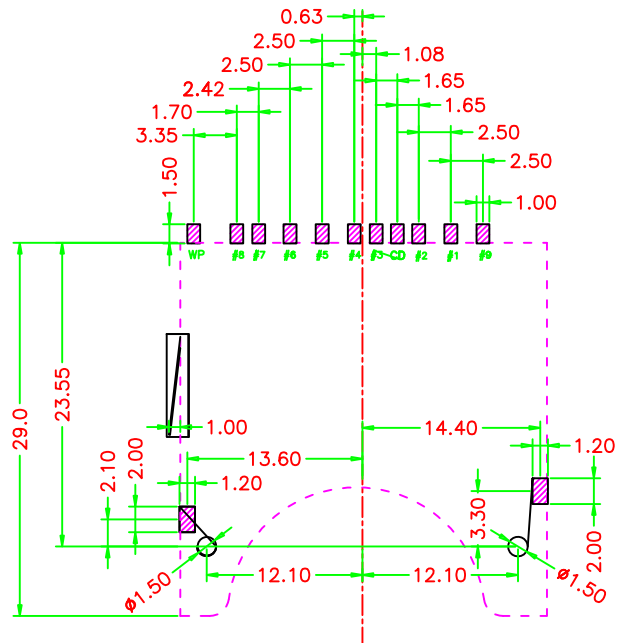
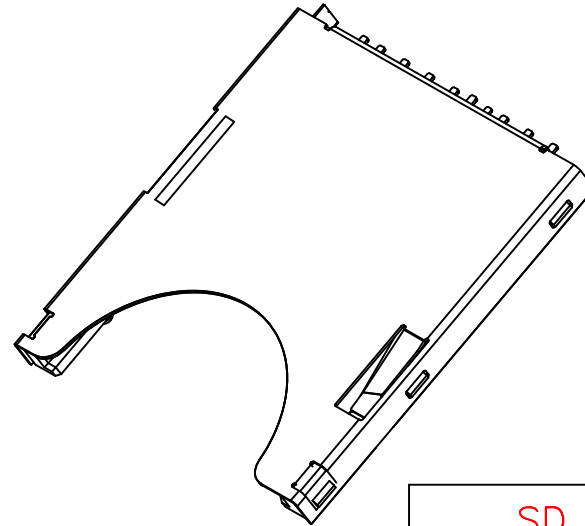




更改 REV	数量 MODIFICATION
A	ECXXXXX



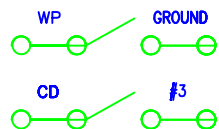
P.C.B LAYOUT



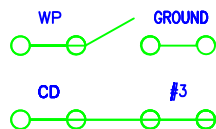
SD CARD PIN DESIGN

PIN NO.	NAME	TYPE	DESCRIPTION
#1	CD/DAT3	I/O/PP	CARD DETECT/DATA LINE(BIT3)
#2	CMD	PP	COMMAND/RESPONSE
#3	VSS1	S	SUPPLY VOLTAGE GROUND
#4	VDD	S	SUPPLY VOLTAGE
#5	CLK	I	CLOCK
#6	VSS2	S	SUPPLY VOLTAGE GROUND
#7	DAT0	I/O/PP	DATA LINE(BIT 0)
#8	DAT1	I/O/PP	DATA LINE(BIT 1)
#9	DAT2	I/O/PP	DATA LINE(BIT 2)

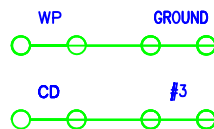
CIRCUIT



WITHOUT CARD



CARD INSERTED WRITE
PROJECT:LOCK



CARD INSERTED WRITE
PROJECT:UN LOCK



GENERAL TOLERANCE		KINGCONN 皇海科技股份有限公司	
X± 0.50	X*± 5 °	名 稱(TITLE) SD PUSH G/F	
.X± 0.30	.X*± 2 °		
.XX± 0.20	.XX*± 1 °		
單 位(UNIT) mm	料 號(PART NO.) 7SDCN-F0-0028	圖 號(DWG NO.) 7SDCN-F0-0028	比例(SCALE) 1:1
審 核(APPROVAL)	核 對(CHECKED)	製 圖(DRAWN)	張 數(SHEET) 2 / 2
		時 間(REV)	更 改 REV A